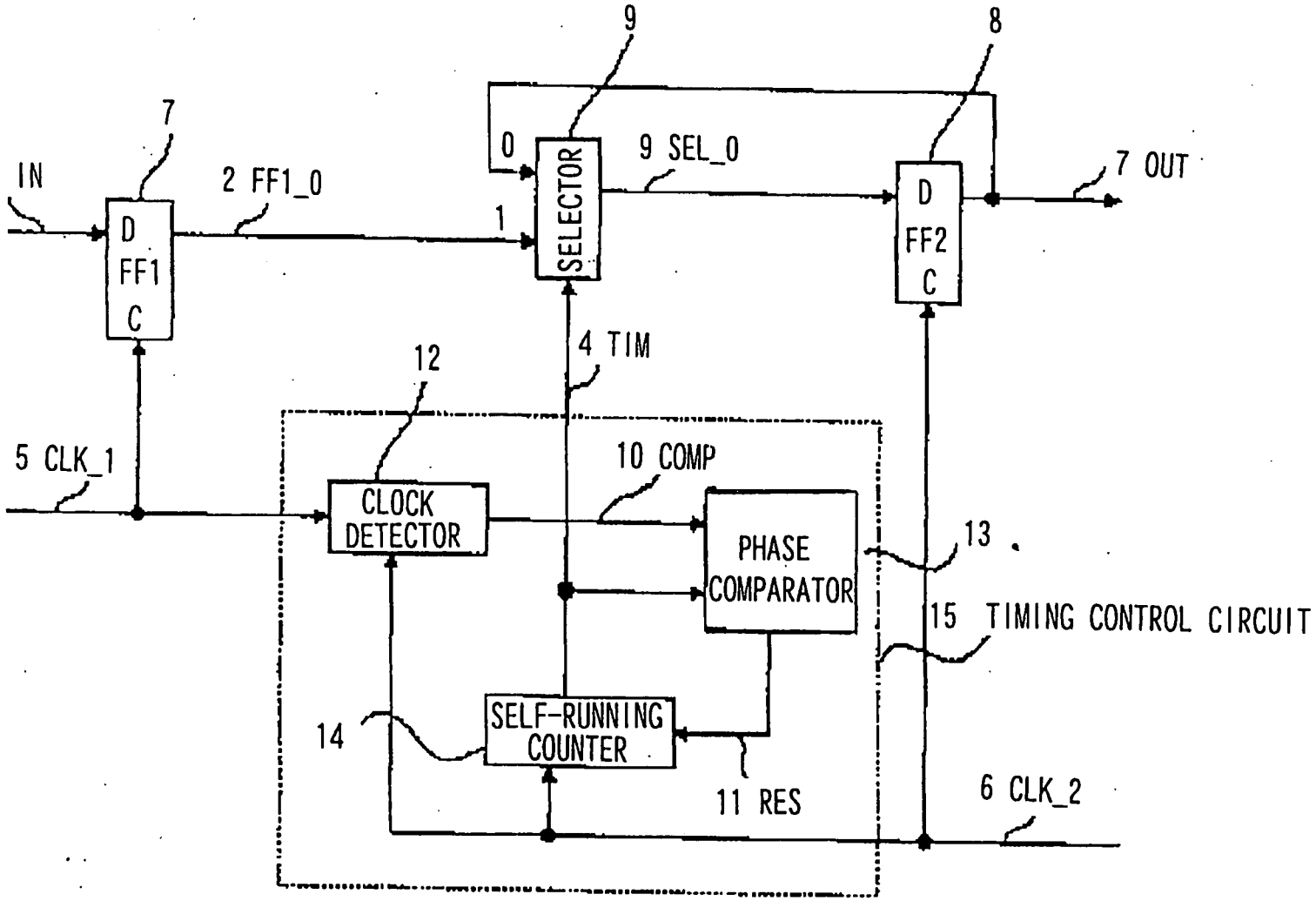
FIG . 1


1 IN
7
D
FF1
C
2 FF1_0
0
1
9
SELECTOR
9 SEL_0
8
D
FF2
C
7 OUT
4 TIM
12
5 CLK_1
CLOCK
DETECTOR
10 COMP
PHASE
COMPARATOR
13
15 TIMING CONTROL CIRCUIT
14
SELF-RUNNING
COUNTER
11 RES
6 CLK_2